

CLAIMS

What is claimed is:

1. An apparatus, comprising:
 - a request queue coupled to a memory unit via a memory-sensing device;
 - a response queue coupled to the memory-sensing device; and
 - an arbiter coupled to said response queue.
2. The apparatus of claim 1, wherein the memory-sensing device comprises redundant circuitry capable of sensing memory in the memory unit substantially simultaneously.
3. The apparatus of claim 1, wherein said request queue comprises memory to store more than one request.
4. The apparatus of claim 3, wherein the memory to store more than one request comprises memory to service more than one request substantially simultaneously.
5. The apparatus of claim 1, wherein said response queue comprises memory to store data for a response.
6. The apparatus of claim 1, wherein said arbiter comprises a response arbiter to determine a response to more than one request.
7. The apparatus of claim 6, wherein the response arbiter comprises a priority determiner to determine a priority of a response to a request.
8. The apparatus of claim 1, wherein said arbiter comprises a request arbiter coupled to said request queue.

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1 21. A system, comprising:

2 a virtual-port memory device coupled to a microprocessor; and
3 an input-output device coupled to the microprocessor.

1 22. The system of claim 21, wherein said virtual-port memory device comprises:

2 a request queue coupled to a memory unit via a memory sensing device;
3 a response queue coupled to the memory sensing device; and
4 an arbiter coupled to said response queue.

1 23. The system of claim 21, wherein said input-output device comprises an antenna
2 device.

1 24. The system of claim 21, wherein said input-output device comprises an audio
2 input device and an audio output device.

